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Part I, "Circuit Design," examines in detail the background and coding techniques of VHDL, including code structure, data types, operators and attributes, concurrent and sequential statements and code, objects (signals, variables, and constants), design of finite state machines, and examples of additional circuit designs. Part II, "System Design," builds on the material already presented, adding elements intended mainly for library allocation; it examines packages and components, functions ...

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Prof. Volnei A. Pedroni. The author received his MSc and PhD degrees in Electrical Engineering from the California Institute of Technology (Caltech), with emphasis on VLSI design, analog and digital. He is a Professor of Electronics Engineering at Brazil's UTFPR and a regular Visiting Faculty of Electrical Engineering at Caltech. He has done collaboration also with other Institutions, in USA and Italy (he is a Brazilian and Italian citizen).

About the Author | Circuit Design with VHDL by Volnei A ...

The book is organized in a clear progression, with the first part covering the circuit level, treating foundations of VHDL and fundamental coding, and the second part covering the system level (units that might be located in a library for code sharing, reuse, and partitioning), expanding upon the earlier chapters to discuss system coding. Part I, "Circuit Design," examines in detail the background and coding techniques of VHDL, including code structure, data types, operators and attributes

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